

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): HSU et al.

SERIAL NO.: Not Yet Assigned

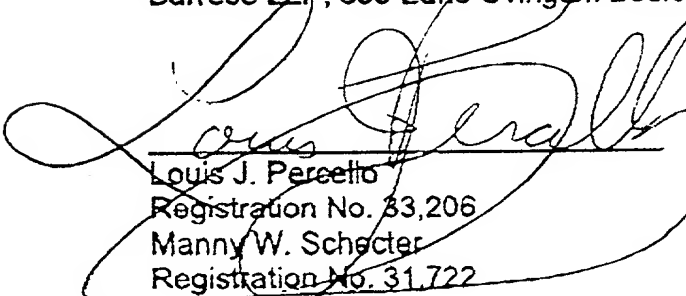
FILED: Concurrently Herewith

FOR: SEMICONDUCTOR MEMORY SYSTEM HAVING DYNAMICALLY  
DELAYED TIMING FOR HIGH-SPEED DATA TRANSFERS

**ASSOCIATE POWER OF ATTORNEY**

Please recognize PETER G. DILWORTH, Reg. No. 26,450; ROCCO S. BARRESE, Reg. No. 25,253; DAVID M. CARTER, Reg. No. 30,949; PAUL J. FARRELL, Reg. No. 33,494; PETER DELUCA, Reg. No. 32,978; JEFFREY S. STEEN, Reg. No. 32,063; ADRIAN T. CALDERONE, Reg. No. 31,746; GEORGE M. KAPLAN, Reg. No. 28,375; JOSEPH W. SCHMIDT, Reg. No. 36,920; RAYMOND E. FARRELL, Reg. No. 34,816; RUSSELL R. KASSNER, Reg. No. 36,183; CHRISTOPHER G. TRAINOR, Reg. No. 39,517; GEORGE LIKOUREZOS, Reg. No. 40,067; EDWARD C. MEAGHER, Reg. No. 41,189; MICHAEL E. CARMEN, Reg. No. 43,533; HAROLD G. FURLOW, Reg. No. 43,621; DANIEL E. TIERNEY, Reg. No. 33,461; MICHAEL J. MUSELLA, Reg. No. 39,310; JUDY NAAMAT, Reg. No. 39,311; and MICHAEL R. BREW, Reg. No. 43,513; each of them of DILWORTH & BARRESE, LLP 333 Earle Ovington Boulevard, Uniondale, New York 11553 as associate attorneys in the above-mentioned application, with full power to prosecute said application, to make alterations and amendments therein, and to transact all business in the Patent and Trademark Office connected therewith.

All written communications are to be sent to Paul J. Farrell, Dilworth & Barrese LLP, 333 Earle Ovington Boulevard, Uniondale, New York 11553.

  
Louis J. Percetto  
Registration No. 33,206  
Manny W. Schecter  
Registration No. 31,722

Dated: 8/27/01